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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/779,353	02/08/2001	Angel Pino	68996	5776	
23872	7590 01/06/2005		EXAMINER		
MCGLEW & TUTTLE, PC 1 SCARBOROUGH STATION PLAZA			DAY, HERNG DER		
	OUGH, NY 10510-0827		ART UNIT	PAPER NUMBER	
·			2128	2128	

DATE MAILED: 01/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
Office Action Summary		09/779,353	PINO ET AL.		
		Examiner	Art Unit		
		Herng-der Day	2128		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a rep operiod for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statut reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be timely within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from e, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).		
Status					
1) Responsive to communication(s) filed on 08 February 2001.					
· <u> </u>	• • • • • • • • • • • • • • • • • • • •				
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposit	ion of Claims				
5)⊠ 6)⊠ 7)⊠	Claim(s) 1-18 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) 18 is/are allowed. Claim(s) 1 and 12 is/are rejected. Claim(s) 2-11 and 13-17 is/are objected to. Claim(s) are subject to restriction and/or election requirement.				
Applicati	ion Papers				
10)⊠	The specification is objected to by the Examine The drawing(s) filed on 14 June 2001 is/are: a Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Example 2015.	a) accepted or b) objected to drawing(s) be held in abeyance. See tion is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).		
Priority ι	ınder 35 U.S.C. § 119				
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachmen	t(s)				
2) 🔲 Notic 3) 🔲 Inforr	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	(PTO-413) ite atent Application (PTO-152)		

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DETAILED ACTION

1. Claims 1-18 have been examined.

Drawings

- 2. The drawings are objected to for the following reasons. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application.
- **2-1.** Fig. 5 and Fig. 6A-6E should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).
- 2-2. It appears that "5VAOC", as shown in Fig. 6D, should be "5VADC".

Claim Rejections - 35 USC § 112

- 3. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 4. Claim 12 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- **4-1.** Claim 12 recites the limitation "said DSP board" in line 2 of the claim. There is insufficient antecedent basis for this limitation in the claim. For the purpose of claim examination, the Examiner will presume that "said DSP board" refers to "said DSP evaluation board".

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Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

- 6. Claim 1 is rejected under 35 U.S.C. 102(e) as being anticipated by Ohsuga et al., U.S. Patent 6,434,690 B1 issued August 13, 2002 and filed January 11, 1999.
- **6-1.** Regarding claim 1, Ohsuga et al. disclose an emulating interface arrangement comprising:

a bridge board (microcomputer 1, FIG. 1);

a processor connection extending from said bridge board for connecting to address lines, data lines and control lines of a processor evaluation board (CPU core 2, FIG. 1);

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a Digital Signal Processor (DSP) connection extending from said bridge board for connecting to address lines, data lines and control lines of a DSP evaluation board (DSP Engine 3, FIG. 1);

a Programmable Logic Device (PLD) mounted on said bridge board and electrically connected to both said processor connection and said DSP connection (controllers, FIG. 1);

software included in said PLD for reading said address lines, said data lines and said control lines of said processor connection and said DSP connection, said software monitoring said lines and converting signals from said address lines, data lines and control lines of said processor connection into DSP transfer signals based on signals received from said DSP connection, said software transmitting said DSP transfer signals to said DSP connection, said software converting signals from said address lines, said data lines and said control lines of said DSP connection into processor transfer signals based on signals received from said processor connection, said software transmitting said processor transfer signals to said processor connection (erasable programmable memories, address bus, data bus, control bus, column 6, lines 6-28).

Allowable Subject Matter

- 7. Claim 18 is allowed.
- 8. Claim 12 would be allowable if rewritten to overcome the rejections under 35 U.S.C. 112, second paragraph, set forth in this Office action and to include all of the limitations of the base claim and any intervening claims.

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9. Claims 2-11 and 13-17 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

10. The prior art made of record and not relied upon is considered pertinent to Applicant's disclosure. Reference to Yamaura et al., U.S. Patent 5,594,890 issued January 14, 1997, is cited as disclosing an emulation system for emulating CPU core.

Reference to Bonola, U.S. Patent 5,953,516 issued September 14, 1999, is cited as disclosing a method for emulating a peripheral device.

Reference to So et al., U.S. Patent 6,179,489 B1 issued January 30, 2001, is cited as disclosing a method for coordination of computer main microprocessor and second microprocessor.

11. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Herng-der Day whose telephone number is (571) 272-3777. The Examiner can normally be reached on 9:00 - 17:30.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Jean R. Homere can be reached on (571) 272-3780. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

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applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private

PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Herng-der Day H.D. December 27, 2004

May/han Thai Phan Betent Examiner Au: 2128

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